	Туре	L #	Hits	Search Text	DBs
1	BRS	L2	3	framer\$2 adj5 fpga	USPAT
2	BRS ·	L3	12	framer\$2 adj5 asic	USPAT
3	BRS	L4	3	3 and @AD<=19990528 and tsi	USPAT
4	BRS	L5	2	4 and connectivity	USPAT
5	BRS	L8	6	(fpga or asic) same test adj10 connectivity	USPAT
6	BRS	L7	11	(fpga or asic) adj10 generat\$4 same slot	USPAT

	<del></del>	<del></del>		4	1	Search Text	DBs
	Ty	ype	L #		its	cisco.as. and time adj5 stream	JSPAT
	BRS		L2	16		and 199\$1.fy.	USPAT
;	BRS	5	L3	60	349	@AD<=19990528  370/422.ccls. and tdm and	USPAT
3	BRS	3	L4	4		unused and @AD<=19990526	
 1	BR	S	L5	19	Э	370/422.ccls. and transmission adj system and @AD<=19990528	USPAT
 5	BR	S	L6	0		5 and fpga	USPAT
 6	BR	.S	L7	2		5 and tdm	USPAT
- - 7	BF		L8	1	6	370/459.ccls. and transmission adj system and @AD<=19990528	USPAT
8	BI	RS	L9	5	 )	8 and tdm	USPAT
-		 RS	L10	(	)	370/459.ccls. and fpga and @AD<=19990528	USPAT
9		RS RS	L11		3039	fpga and @AD<=19990528	USPAT
-		RS RS	L12		10	11 and tdm and tsi	USPAT
F			L1'		10	12 and slot	USPAT
-		RS	L1		1	transmission adj card and framers and @AD<=19990528	USPAT
+		3RS	L1		0	framer\$2 adj10 fpga same ts1	USPAT
-		BRS				and @AD<=19990528  framer\$2 and fpga same tsi a	nd <sub>USPAT</sub>
-		BRS BRS	L2		4	@AD<=19990528  time adj slot adj interchang and fpga and controller and @AD<=19990528	er USPAT
}	17	BRS	L	22	2	time adj slot adj interchang same fpga and @AD<=19990528	Jer USPAT
	18	BRS	L	23	2	(time adj slot adj interchanger) same fpga and @AD<=19990528	USPAT
	19	BRS	L	24	371	(time adj slot adj interchanger) and @AD<=19990528	USPAT
	20	BRS	I	.25	10	24 and insert\$4 adj10 test	USPAT
	21	BRS	I	 _26	72	370/376.ccls. and 24	USPAT